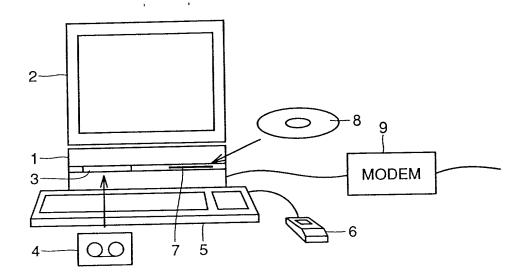
FIG.1





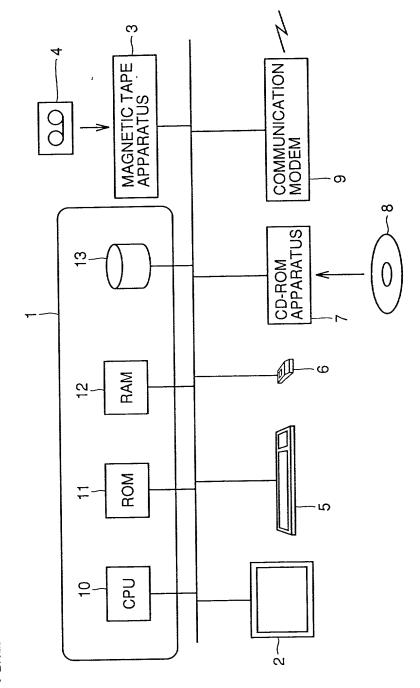


FIG.3

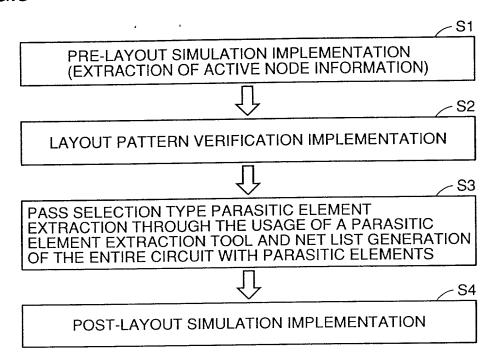
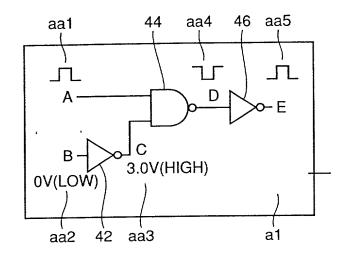


FIG.4A



aa1 a2 aa4 aa5

VDD

A

OV

(LOW)

F

GND

ACTIVE NODES DETECTED AT
THE PRE-LAYOUT SIMULATION

FIG.4C

A,D,E,F aa6

FIG.5A

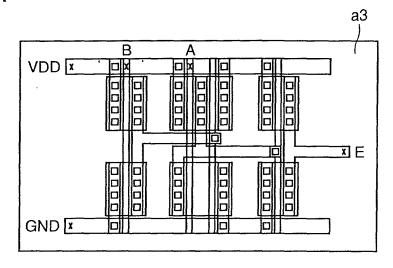
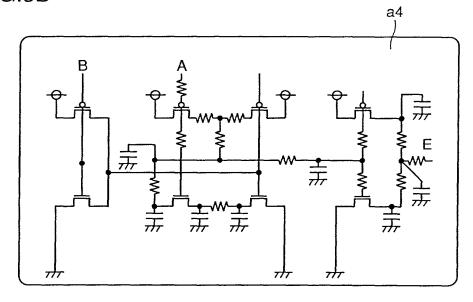


FIG.5B



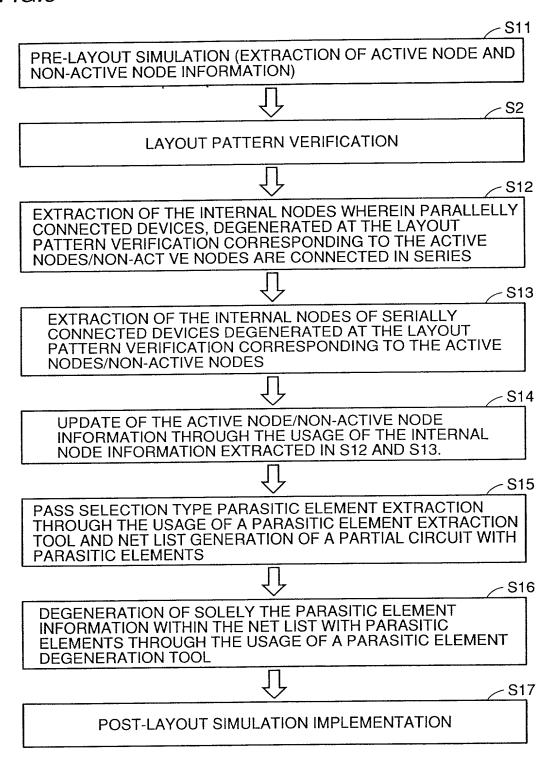


FIG.7A

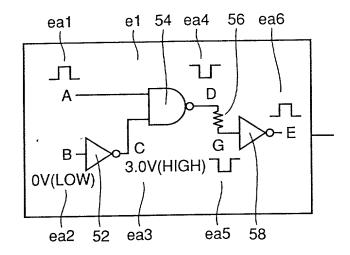


FIG.7B

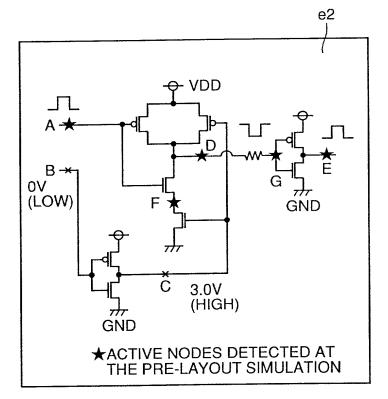


FIG.7C

(ACTIVE NODE INFORMATION)

A,D,E,F,G ea7

FIG.7D

(NON-ACTIVE NODE INFORMATION)

C 3.0V -ea8

FIG.8A

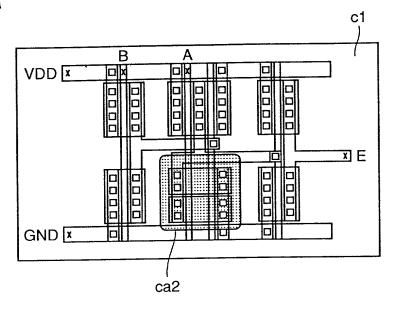
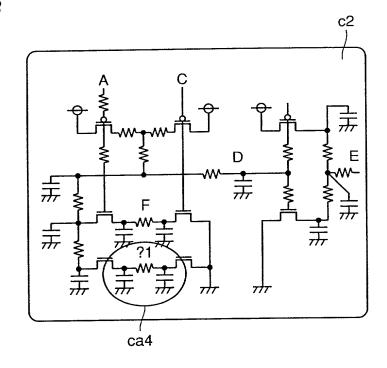


FIG.8B



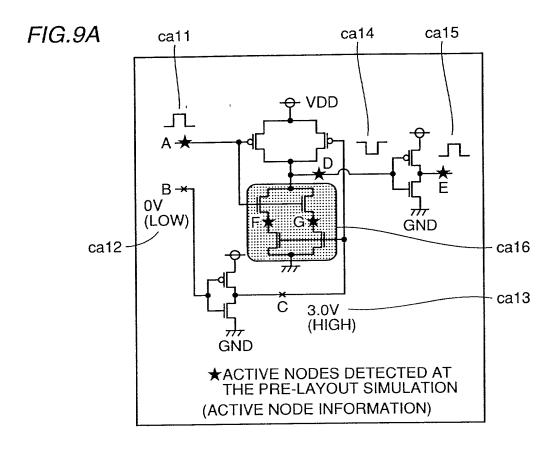


FIG.9B

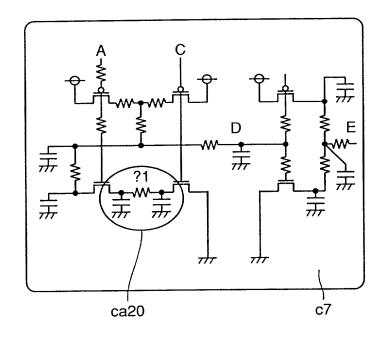


FIG.10A

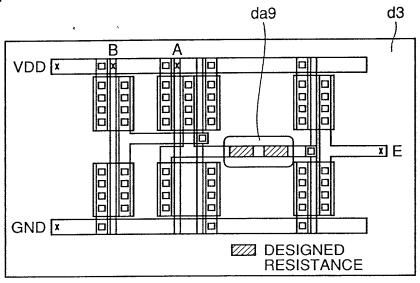
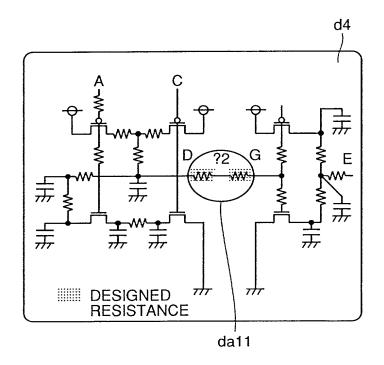


FIG.10B





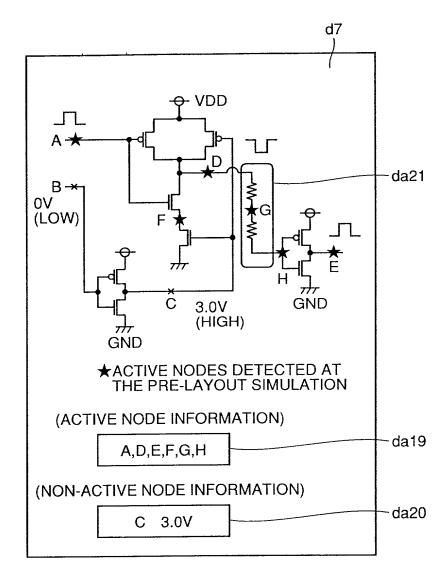
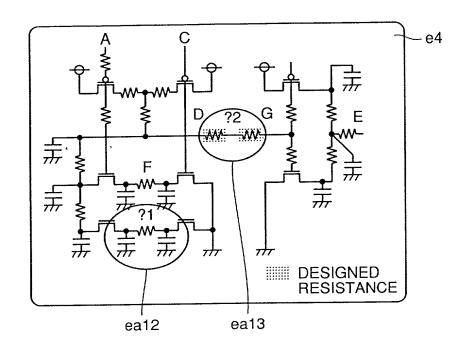


FIG.12A



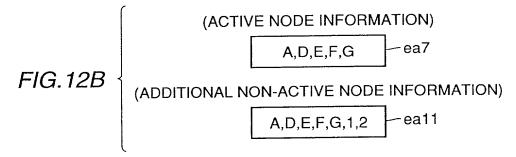


FIG.12C

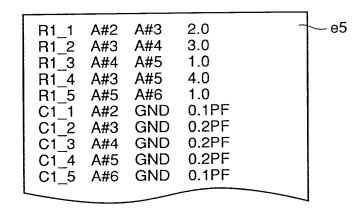


FIG.12D

